

Inventor Search
Inventor[IEEE HOME](#) | [SEARCH IEEE](#) | [SHOP](#) | [WEB ACCOUNT](#) | [CONTACT IEEE](#)[Membership](#) [Publications/Services](#) [Standards](#) [Conferences](#) [Careers/Jobs](#)**IEEE Xplore™**
RELEASE 1.4[Help](#) [FAQ](#) [Terms](#) [IEEE Peer Review](#)[Quick Links](#)» [Se](#)

Welcome to IEEE Xplore™

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account

Print Format

Your search matched **16** of **759985** documents.Results are shown **15** to a page, sorted by **publication year** in **descending** order.**Results:**Journal or Magazine = **JNL** Conference = **CNF** Standard = **STD****1 Circuit design for a 2.2 GB/s memory interface***Sidiropoulos, S.; Abhyankar, A.; Chen, C.; Chang, K.; Tsu-Ju Chin; Hays, N.; Ying Li; Tsang, G.; Wong, A.; Stark, D.*

Solid-State Circuits Conference, 2001. Digest of Technical Papers. ISSCC. 200 International , 2001

Page(s): 70 -71, 433

[\[Abstract\]](#) [\[PDF Full-Text \(188 KB\)\]](#) **CNF****2 A variable-frequency parallel I/O interface with adaptive power-su regulation***Gu-Yeon Wei; Kim, J.; Liu, D.; Sidiropoulos, S.; Horowitz, M.A.*

Solid-State Circuits, IEEE Journal of , Volume: 35 Issue: 11 , Nov. 2000

Page(s): 1600 -1610

[\[Abstract\]](#) [\[PDF Full-Text \(240 KB\)\]](#) **JNL****3 Adaptive bandwidth DLLs and PLLs using regulated supply CMOS b***Sidiropoulos, S.; Dean Liu; Jaeha Kim; Guyeon Wei; Horowitz, M.*

VLSI Circuits, 2000. Digest of Technical Papers. 2000 Symposium on , 2000

Page(s): 124 -127

[\[Abstract\]](#) [\[PDF Full-Text \(344 KB\)\]](#) **CNF****4 A variable-frequency parallel I/O interface with adaptive power su regulation***Gu-Yeon Wei; Jaeha Kim; Liu, D.; Sidiropoulos, S.; Horowitz, M.*

Solid-State Circuits Conference, 2000. Digest of Technical Papers. ISSCC. 200 International , 2000

Page(s): 298 -299

[\[Abstract\]](#) [\[PDF Full-Text \(116 KB\)\]](#) **CNF****5 A multiple vendor 2.5-V DLL for 1.6-GB/s RDRAMs**

Portmann, C.; Chu, A.; Hays, N.; Sidiropoulos, S.; Stark, D.; Chau, P.; Donn Garlepp, B.

VLSI Circuits, 1999. Digest of Technical Papers. 1999 Symposium on , 1999
Page(s): 153 -156

[\[Abstract\]](#) [\[PDF Full-Text \(336 KB\)\]](#) **CNF**

6 High-speed electrical signaling: overview and limitations

Horowitz, M.; Chih-Kong Ken Yang; Sidiropoulos, S.

IEEE Micro , Volume: 18 Issue: 1 , Jan.-Feb. 1998

Page(s): 12 -24

[\[Abstract\]](#) [\[PDF Full-Text \(196 KB\)\]](#) **JNL**

7 A 2 Gb/s/pin CMOS asymmetric serial link

Kun-Yung Ken Chang; Ellersick, W.; Shang-Tse Chuang; Sidiropoulos, S.; Ho M.

VLSI Circuits, 1998. Digest of Technical Papers. 1998 Symposium on , 1998
Page(s): 216 -217

[\[Abstract\]](#) [\[PDF Full-Text \(304 KB\)\]](#) **CNF**

8 A 700-Mb/s/pin CMOS signaling interface using current integrating receivers

Sidiropoulos, S.; Horowitz, M.

Solid-State Circuits, IEEE Journal of , Volume: 32 Issue: 5 , May 1997

Page(s): 681 -690

[\[Abstract\]](#) [\[PDF Full-Text \(224 KB\)\]](#) **JNL**

9 A semidigital dual delay-locked loop

Sidiropoulos, S.; Horowitz, M.A.

Solid-State Circuits, IEEE Journal of , Volume: 32 Issue: 11 , Nov. 1997

Page(s): 1683 -1692

[\[Abstract\]](#) [\[PDF Full-Text \(272 KB\)\]](#) **JNL**

10 A semi-digital DLL with unlimited phase shift capability and 0.08-4 operating range

Sidiropoulos, S.; Horowitz, M.

Solid-State Circuits Conference, 1997. Digest of Technical Papers. 43rd ISSCC
IEEE International , 1997

Page(s): 332 -333, 481

[\[Abstract\]](#) [\[PDF Full-Text \(1316 KB\)\]](#) **CNF**

11 A speed, power, and supply noise evaluation of ECL driver circuits*Jouppi, N.P.; Sidiropoulos, S.; Menon, S.*

Solid-State Circuits, IEEE Journal of , Volume: 31 Issue: 1 , Jan. 1996

Page(s): 38 -45

[\[Abstract\]](#) [\[PDF Full-Text \(660 KB\)\]](#) **JNL****12 A 700 Mbps/pin CMOS signalling interface using current integratin receivers***Sidiropoulos, S.; Horowitz, M.*

VLSI Circuits, 1996. Digest of Technical Papers., 1996 Symposium on , 1996

Page(s): 142 -143

[\[Abstract\]](#) [\[PDF Full-Text \(180 KB\)\]](#) **CNF****13 Current integrating receivers for high speed system interconnects***Sidiropoulos, S.; Horowitz, M.*

Custom Integrated Circuits Conference, 1995., Proceedings of the IEEE 1995

Page(s): 107 -110

[\[Abstract\]](#) [\[PDF Full-Text \(284 KB\)\]](#) **CNF****14 A speed, power, and supply noise evaluation of ECL driver circuits***Sidiropoulos, S.; Jouppi, N.P.; Menon, S.*

Bipolar/BiCMOS Circuits and Technology Meeting, 1994., Proceedings of the 1994

Page(s): 119 -122

[\[Abstract\]](#) [\[PDF Full-Text \(272 KB\)\]](#) **CNF****15 A Cmos 500 Mbps/pin Synchronous Point to Point Link Interface***Sidiropoulos, S.; Chih-Kong Ken Yang; Horowitz, M.*

VLSI Circuits, 1994. Digest of Technical Papers., 1994 Symposium on

Page(s): 43 -44

[\[Abstract\]](#) [\[PDF Full-Text \(168 KB\)\]](#) **CNF**[1](#) [2](#) [\[Next\]](#)

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#)
[Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#)
[No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2002 IEEE — All rights reserved

[IEEE HOME](#) | [SEARCH IEEE](#) | [SHOP](#) | [WEB ACCOUNT](#) | [CONTACT IEEE](#)[Membership](#) | [Publications/Services](#) | [Standards](#) | [Conferences](#) | [Careers/Jobs](#)**IEEE Xplore™**
RELEASE 1.4[Help](#) | [FAQ](#) | [Terms](#) | [IEEE Peer Review](#)[Quick Links](#)» [Se](#)**Welcome to IEEE Xplore™**

- ☐ [Home](#)
- ☐ [What Can I Access?](#)
- ☐ [Log-out](#)

Tables of Contents

- ☐ [Journals & Magazines](#)
- ☐ [Conference Proceedings](#)
- ☐ [Standards](#)

Search

- ☐ [By Author](#)
- ☐ [Basic](#)
- ☐ [Advanced](#)

Member Services

- ☐ [Join IEEE](#)
- ☐ [Establish IEEE Web Account](#)

[Print Format](#)Your search matched **16** of **759985** documents.Results are shown **15** to a page, sorted by **publication year** in **descending** order.**Results:**Journal or Magazine = **JNL** Conference = **CNF** Standard = **STD****16 Weighted round-robin cell multiplexing in a general-purpose ATM chip***Katevenis, M.; Sidiropoulos, S.; Courcoubetis, C.*Selected Areas in Communications, IEEE Journal on , Volume: 9 Issue: 8 , 0
Page(s): 1265 -1279[\[Abstract\]](#) | [\[PDF Full-Text \(1448 KB\)\]](#) | **JNL**[\[Prev\]](#) | [1](#) | [2](#)

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#)
[Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#)
[No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2002 IEEE — All rights reserved